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John G.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**Applicant:** Kazuo Nishiyama **Atty. Docket No.** 075834.00071  
**Serial No.:** 09/ 841,582 **Group Art Unit:** 2827  
**Filed:** April 24, 2001 **Examiner:** Lourdes C. Cruz  
**Invention:** "CHIP-LIKE COMPONENTS, A METHOD OF  
MANUFACTURING THE SAME, A PSEUDO WAFER  
THEREFOR AND A METHOD OF MANUFACTURING  
THEREOF"

**RESPONSE TO ELECTION/RESTRICTION REQUIREMENT**

Assistant Commissioner of Patents  
Washington, D.C. 20231

S I R:

In response to the Office Action dated June 7, 2002, Applicant elects claims 1-9 of Invention I drawn to a semiconductor wafer. Applicant specifically reserves the right to prosecute the non-elected claims in a later filed continuation or divisional application.

Applicant therefore awaits consideration and examination of elected claims 1-9 and respectfully requests consideration on the merits in due course.

**PETITION FOR EXTENSION OF TIME**

Applicant hereby petitions for a one-month extension of time to respond to the outstanding Office Action under 37 C.F.R. §1.136(a). The time to respond is thus extended to August 21, 2002. Applicant has also included a check in the amount of \$110.00 as payment of the required fee set forth in 37 C.F.R. §1.17(a).

Respectfully submitted,

Date: July 25, 2002

Robert J. Depke

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(Reg. #37,807)

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